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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
ATTEICATION NO.	TIENG BATE	TIKST WANED INVENTOR	ATTORNET DOCKET NO.	CONTINUATION NO.
10/788,465	03/01/2004	Takayoshi Yoshida	042151	9261
38834 7590 05/03/2007 WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP 1250 CONNECTICUT AVENUE, NW SUITE 700 WASHINGTON, DC 20036			EXAMINER	
			ABDULSELAM, ABBAS I	
			ART UNIT	PAPER NUMBER
			2629	
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•			MAIL DATE	DELIVERY MODE
			05/03/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Application No.	Applicant(s)		
Office Action Summary		10/788,465	YOSHIDA ET AL.		
		Examiner	Art Unit		
		Abbas I. Abdulselam	2629		
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet	with the correspondence address		
A SH WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DANS IN THE MAIL	ATE OF THIS COMMUN 36(a). In no event, however, may vill apply and will expire SIX (6) M , cause the application to become	VICATION. a reply be timely filed ONTHS from the mailing date of this communication. ABANDONED (35 U.S.C. § 133).		
Status					
1)⊠	Responsive to communication(s) filed on 14 Fe	ebruary 2007.			
/ _	This action is FINAL . 2b) ☐ This action is non-final.				
3)∟	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is				
	closed in accordance with the practice under E	x paπe Quayle, 1935 C	.D. 11, 453 O.G. 213.		
Disposit	ion of Claims				
5)⊠ 6)⊠ 7)□	Claim(s) 3-5 and 15-17 is/are pending in the ap 4a) Of the above claim(s) is/are withdraw Claim(s) 15 and 17 is/are allowed. Claim(s) 3-5 and 16 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	vn from consideration.			
•—	ion Papers				
10)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) accomplicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Examine	epted or b) objected t drawing(s) be held in abey ion is required if the drawi	rance. See 37 CFR 1.85(a). ng(s) is objected to. See 37 CFR 1.121(d).		
Priority (under 35 U.S.C. § 119				
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.					
2) Notice 3) Infor	et(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO/SB/08) er No(s)/Mail Date	Paper N	v Summary (PTO-413) o(s)/Mail Date if Informal Patent Application		

1. This action is in response to a communication filed on April 21, 2007. Claims 3-5 and 15-

17 are pending. Claims 1-2, 6-14 ad 18-20 are canceled.

Response to Arguments

2. Applicant's arguments filed on 02/14/07 have been fully considered but they are not persuasive.

Applicant argues that the reference cited, Velayudhan et al. (USPN 6803890) does not teach a set period in which the voltage of both ends of the light emitting element whose lighting is to be driven in a scan period is set at a predetermined voltage value at a beginning of said scan period corresponding to one scan line so that drive current for holding the constant intensity state is given to the light emitting element within the intensity increase period.

However, as shown in the art rejection below, Velayudhan teaches as shown in FIG. 3 a Positive Non-Ramped Modulating Pulse (flat plateau). Section 'A' is the rise time (with V.sub.1 -V.sub.ref equaling the threshold voltage) and section 'C' is the fall time and section 'B' is the flat plateau. (Fig. 3, on the time axis corresponding to regions A, B, C, and col. 3, lines 51-53). It is inherent that section B, which is located between the end of the rise time and the beginning of the fall time in the time axis of Fig. 3 has some time elapsed during a constant voltage application.

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Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 3-5 and 16 are rejected under 35 U.S.C. 102(e) as being anticipated by Velayudhan et al. (USPN 6803890).

Regarding claim 3, Velayudhan teaches a drive method for the light emitting display panel in which light emitting elements are connected at respective crossing points between a plurality of data lines, a plurality of scan lines so that the light emitting elements connected to the respective scan lines are sequentially selectively lighted by sequentially scanning the scan lines (Fig. 7 (scan pulse voltage, column drive), Fig. 7 (R1, Rn, C1, Cn, elect5rode row, electrode column), E1 display, Fig. 8, col. 5, lines 24-26 and col. 6, lines 1-6) there is provided the intensity increase period in which a lighting intensity of the light emitting element is gradually increased allowing the lighting intensity to reach the constant intensity state within the predetermined period from the scan start in one scan period, wherein there is provided a set period in which the voltage of both ends of the light emitting element whose lighting is to be driven in a scan period is set at a predetermined voltage value at a beginning of said scan period

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corresponding to one scan line so that drive current for holding the constant intensity state is given to the light emitting element within the intensity increase period (see Fig. 3, on the time axis corresponding to regions A, B, C, and col. 3, lines 51-53).

Regarding claim 4, Velayudhan teaches a drive method for a light emitting display panel in which light emitting elements are connected at respective crossing points between a plurality of data lines, a plurality of scan lines so that the light emitting elements connected to the respective scan lines are sequentially selectively lighted by sequentially scanning the scan lines (Fig. 7 (scan pulse voltage, column drive), Fig. 7 (R1, Rn, C1, Cn, elect5rode row, electrode column), E1 display, Fig. 8, col. 5, lines 24-26 and col. 6, lines 1-6) there is provided the intensity increase period in which a lighting intensity of the light emitting element is gradually increased allowing the lighting intensity to reach the constant intensity state within the predetermined period from the scan start in one scan period, wherein there is provided a set period in which the voltage of both ends of the light emitting element whose lighting is to be driven in a scan period is set at a predetermined voltage value immediately before a scan period corresponding to one scan line so that current which is different from drive current which is for holding the constant intensity state is given to the light emitting element within the intensity increase period (see Fig. 3, on the time axis corresponding to regions A, B, C and col. 3, lines 51-53).

Regarding claim 5, Velayudhan teaches the value of voltage which is applied to both ends of the light emitting element in the set period is set at a voltage value which does not reach

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the forward voltage of the light emitting element in the constant intensity state (see Fig. 3, on the time axis corresponding to regions A, B, C and col. 3, lines 51-53).

Regarding claim 16, Velayudhan teaches a drive device for a light emitting display panel in which light emitting elements are connected at respective crossing points between a plurality of data lines, a plurality of scan lines so that the light emitting elements connected to the respective scan lines are sequentially selectively lighted by sequentially scanning the scan lines (Fig. 7 (scan pulse voltage, column drive), Fig. 7 (R1, Rn, C1, Cn, elect5rode row, electrode column), E1 display, Fig. 8, col. 5, lines 24-26 and col. 6, lines 1-6) there is provided the intensity increase period in which a lighting intensity of the light emitting element is gradually increased allowing the lighting intensity to reach the constant intensity state within the predetermined period from the scan start in one scan period, wherein there is provided a voltage setting means for setting the voltage of both ends of the light emitting element whose lighting is to be driven in a scan period at a predetermined voltage value at a beginning of said scan period corresponding to one scan line so that current from a constant current source which holds the constant intensity state is supplied to the light emitting element in a state in which a constant both end voltage is set in the light emitting element by the voltage setting means.

Allowable Subject Matter

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5. Claims 15 and 17 are allowed.

Conclusion

6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Abbas I. Abdulselam whose telephone number is 7033058591.

The examiner can normally be reached on Monday through Friday from 9:00 A.M. to 5:30 P.M.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe, can be reached on 571-2727691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

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Abbas Abdulselam

Examiner

Art Unit 2629

April 27, 2007

RICHARD HJERPE SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600